

REMARKS

Claims 1-41 are pending in the application.

Claims 1-23, 26-27, 30, 34 and 37 are withdrawn from consideration.

Claims 24-25, 31-33 and 38-41 are allowed.

Claims 28-29 and 35-36 are rejected under 35 U.S.C. § 102(b) as being anticipated by Fogarty et al (USP 4,547,802). Our comments relating to the rejection of claims 28, 29, 35 and 36 are set forth below.

Prior Art Rejection

In rejecting claims 28, 29, 35 and 36 as being anticipated by Fogarty, the Examiner provides comments purporting to show that Fogarty teaches each of the element of the claims. Applicant respectfully disagrees with parts of the Examiner's analysis.

For example, in the discussion of claim 28, the Examiner cites Fig. 1, elements 26 and 28 and col. 6, lines 36-44 of Fogarty as teaching, "determining the variation, during an interval of time, of the direct current (DC) level of a received signal." Applicant submits, however, that the cited teaching of Fogarty merely recognizes that a problem occurs due to charge coupled devices 26 and 28 having a DC component which varies with time (col. 6, lines 37-42), and sees the need to control the DC level of the video output signal (col. 6, lines 42-44). To accomplish this, during the vertical blanking period, the stored charge in coupling capacitors 40, 65 and 67 is adjusted to force the DC level of the video output signal to be equal to the black level of the TV video signal 12 during an unusual line of the vertical blanking interval (col. 6, lines 45-53).

After col. 6, line 53, Fogarty describes in detail how the DC level is adjusted. Nowhere, however, does Fogarty disclose “determining the variation, during an interval of time, of the direct current (DC) level of a received signal.”

Also, the Examiner cites Fig. 1 elements 16, 18, 22 and 30 and Fig. 2 and columns 2-3 of Fogarty as teaching

controlling the operating mode of the equalizer in response to the determined variation, wherein the received signal comprises multi-level symbols representing data and a field synchronizing signal, said symbols being characterized by a DC offset and wherein the determining step further comprises processing the field synchronizing signal to determine the variation of the DC offset in the received signal, wherein the field synchronizing signal comprises a pseudo random number symbol sequence and wherein the processing comprises sampling a part of the pseudo random number symbol sequence.

First, Fogarty does not teach anything relating to an equalizer, much less controlling the operating mode of an equalizer. Second, Fogarty does not disclose processing the field synchronizing signal to determine the variation of the DC offset in the received signal. As argued above, Fogarty does not even determine the variation of the DC offset.

For at least the above reasons, Applicant respectfully submits that claim 25, and its dependent claim 26, are not anticipated by Fogarty.

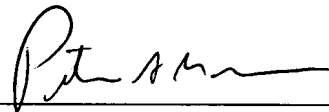
For similar reasons, Applicant submits that claims 35 and 36 are also not anticipated by Fogarty.

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the

Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



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CUSTOMER NUMBER

Date: November 5, 2007